

2826

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Leonard Forbes et al.

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE

Docket No.: 303.324US4

Filed: October 18, 2000

Examiner: Johannes P. Mondt

Serial No.: 09/691,004

Due Date: April 26, 2002

Group Art Unit: 2826

Commissioner for Patents  
Washington, D.C. 20231

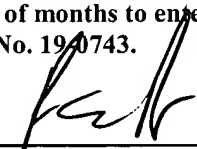
RECEIVED  
MAY - 1 2002  
TECHNOLOGY CENTER 2800

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ A Response to Restriction Requirement (1 Page).
- ☒ A Supplemental Information Disclosure Statement (1 pg.), Form 1449 (1 pg.), and copies of 26 cited references.

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 190743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By:   
Atty: Robert E. Mates  
Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 24th day of April, 2002.

Name Amy Moriarty

Signature Amy Moriarty

**Customer Number 21186**

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
(GENERAL)

P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)